

FIG. 1

1006634-013002

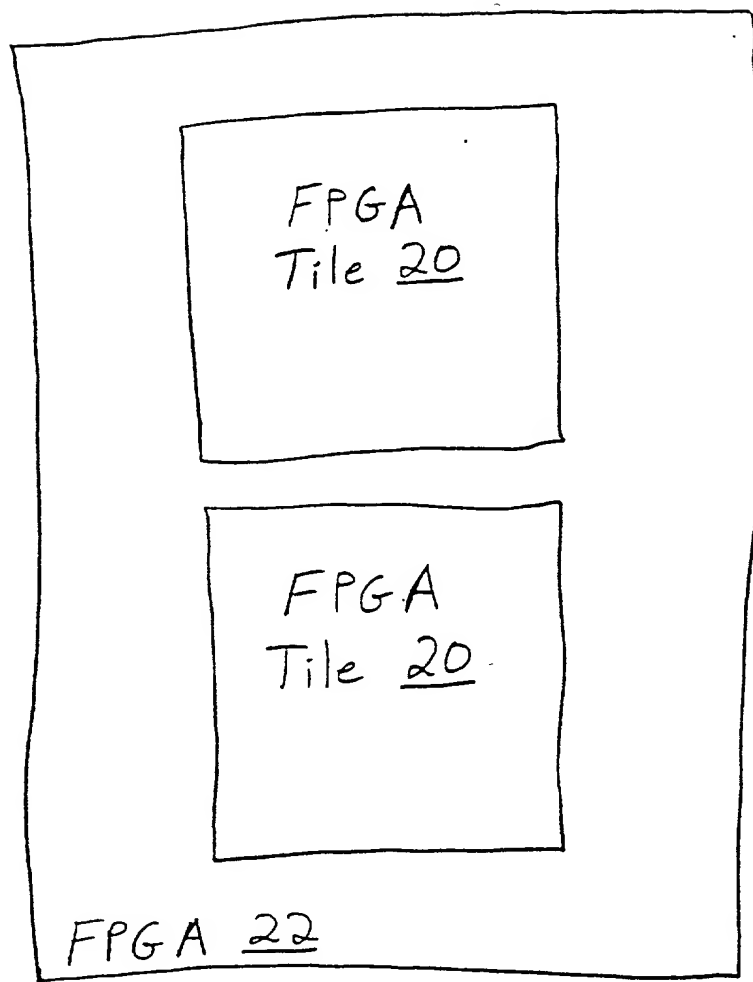


FIG. 2

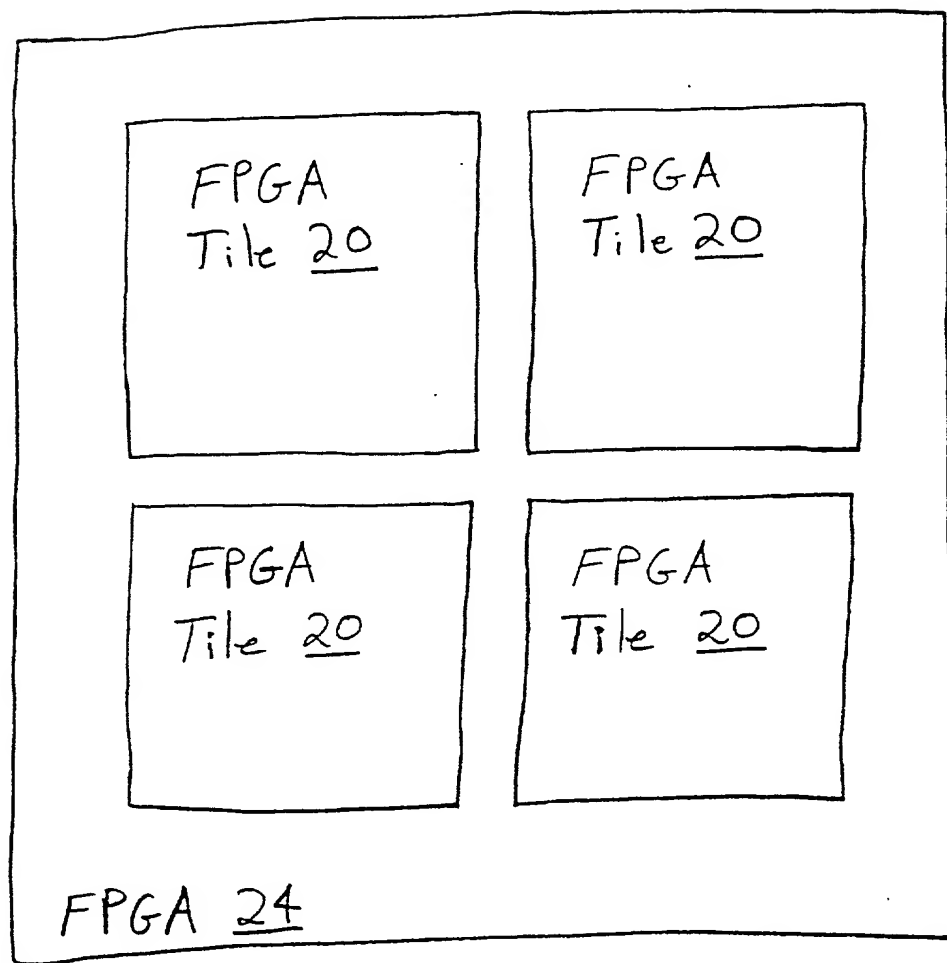


FIG. 3A

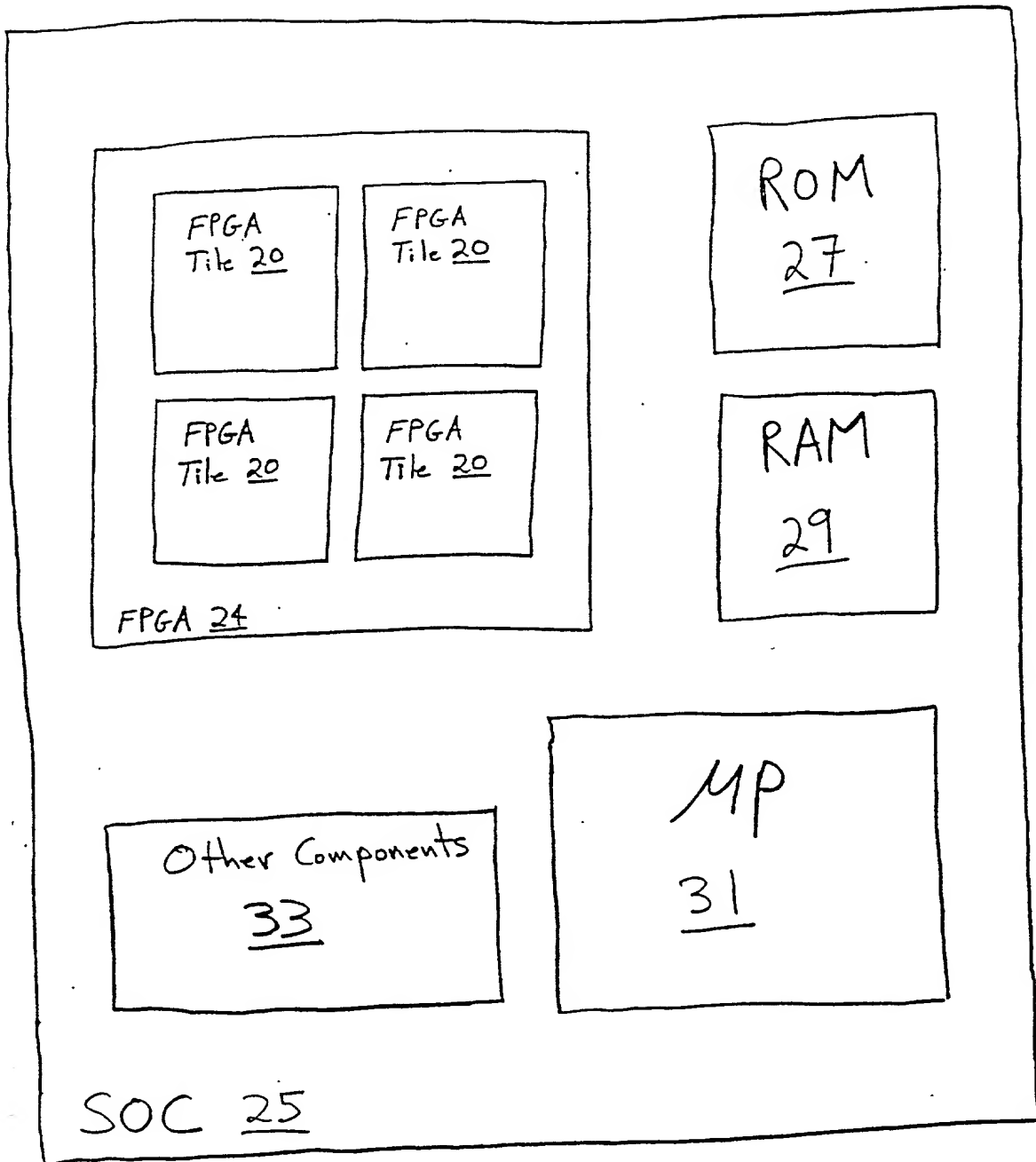


FIG. 3B

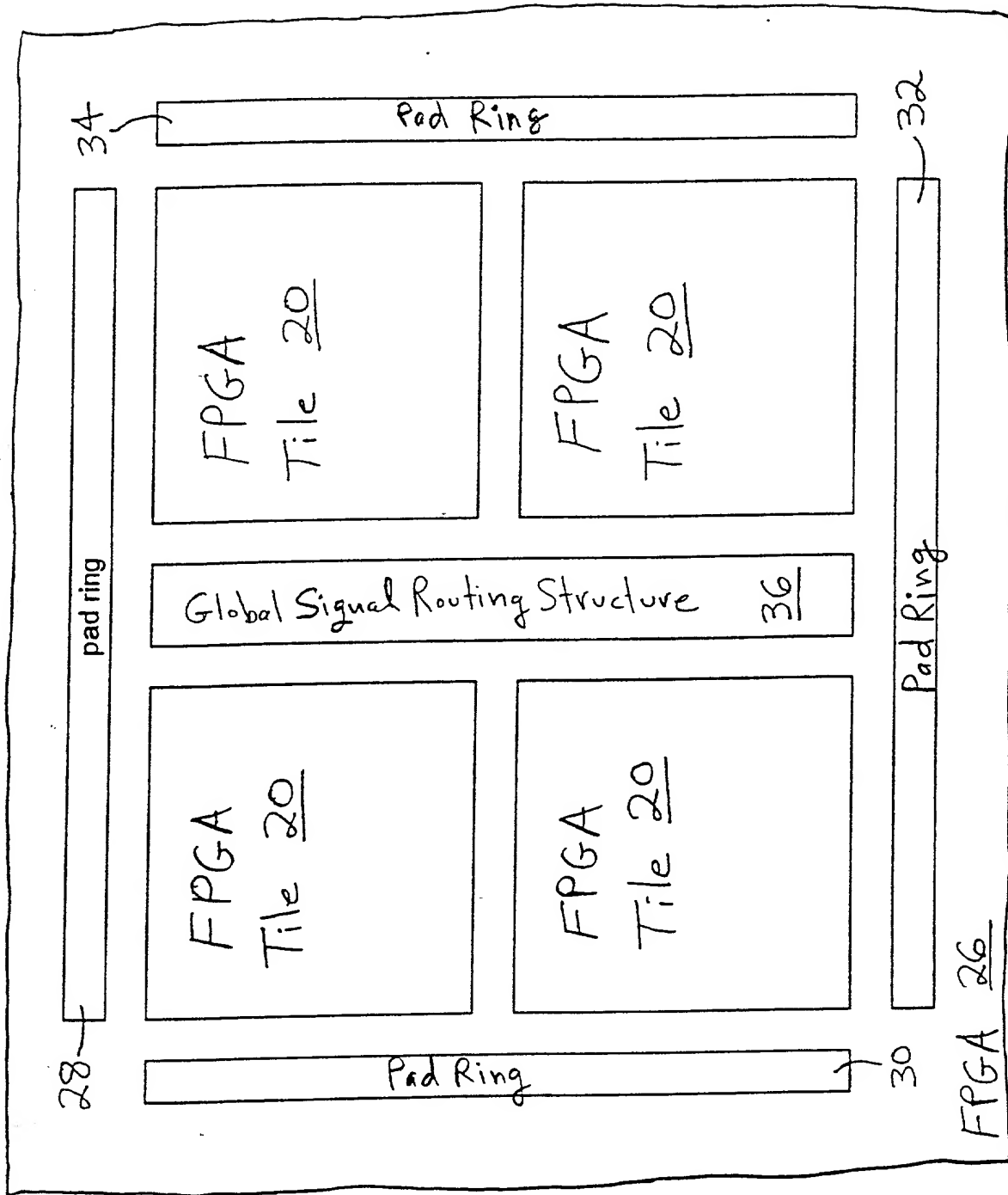


FIG. 4

2006-06-09 14:00:00

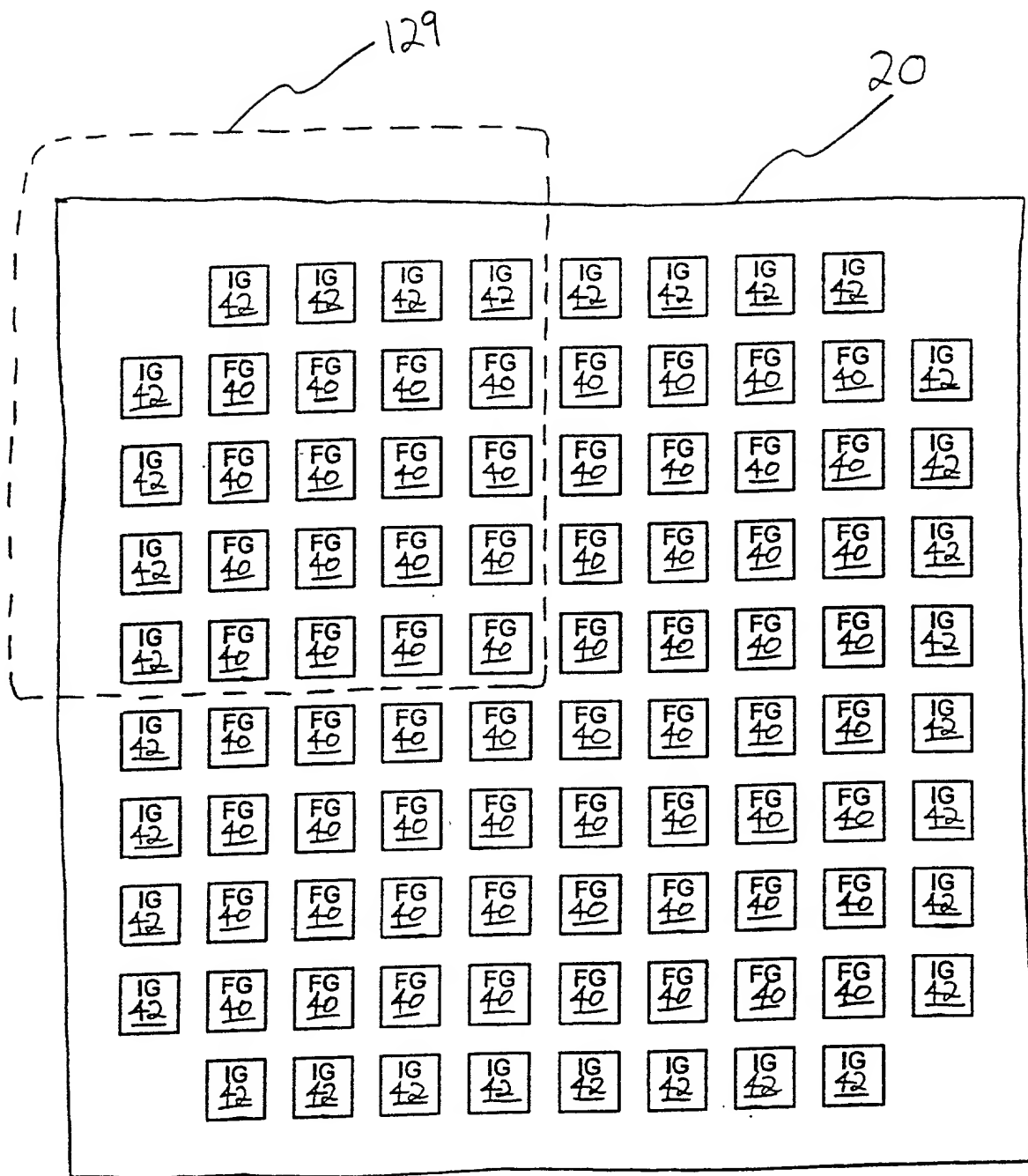


FIG. 5

2006-10-26 09:00:00

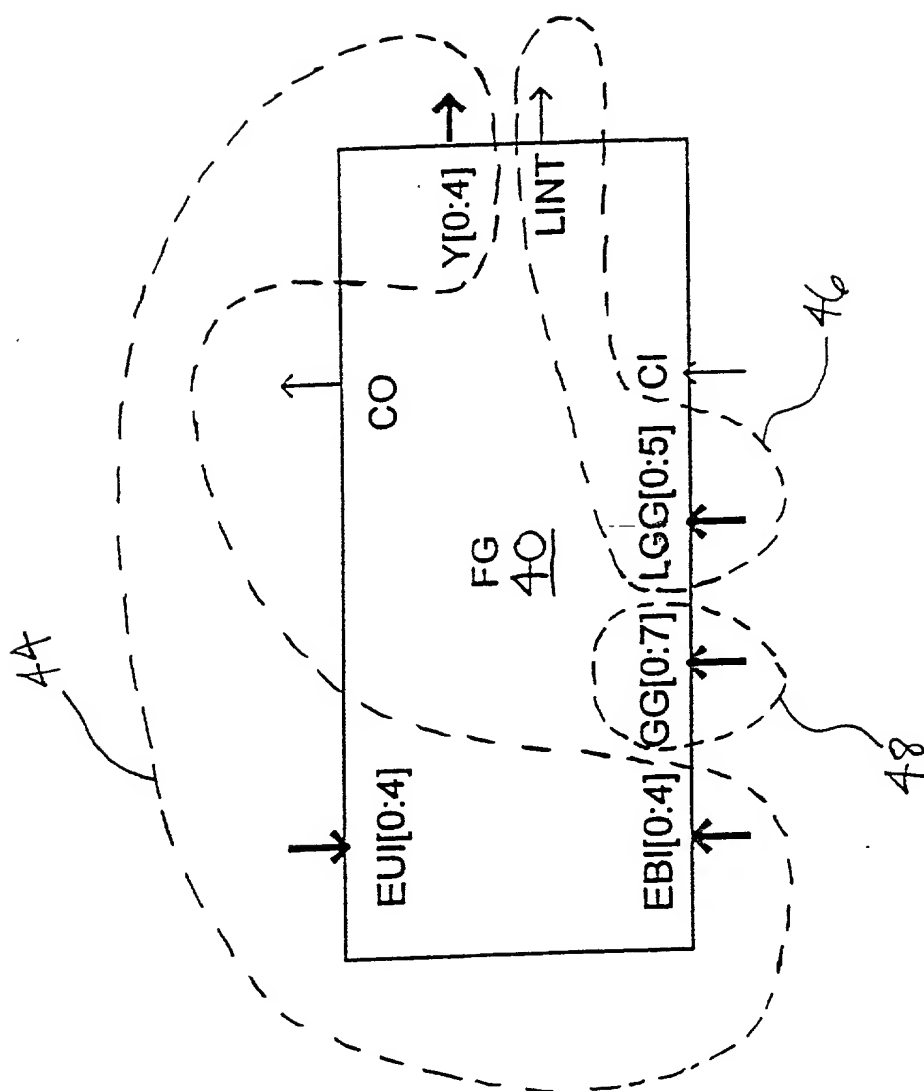
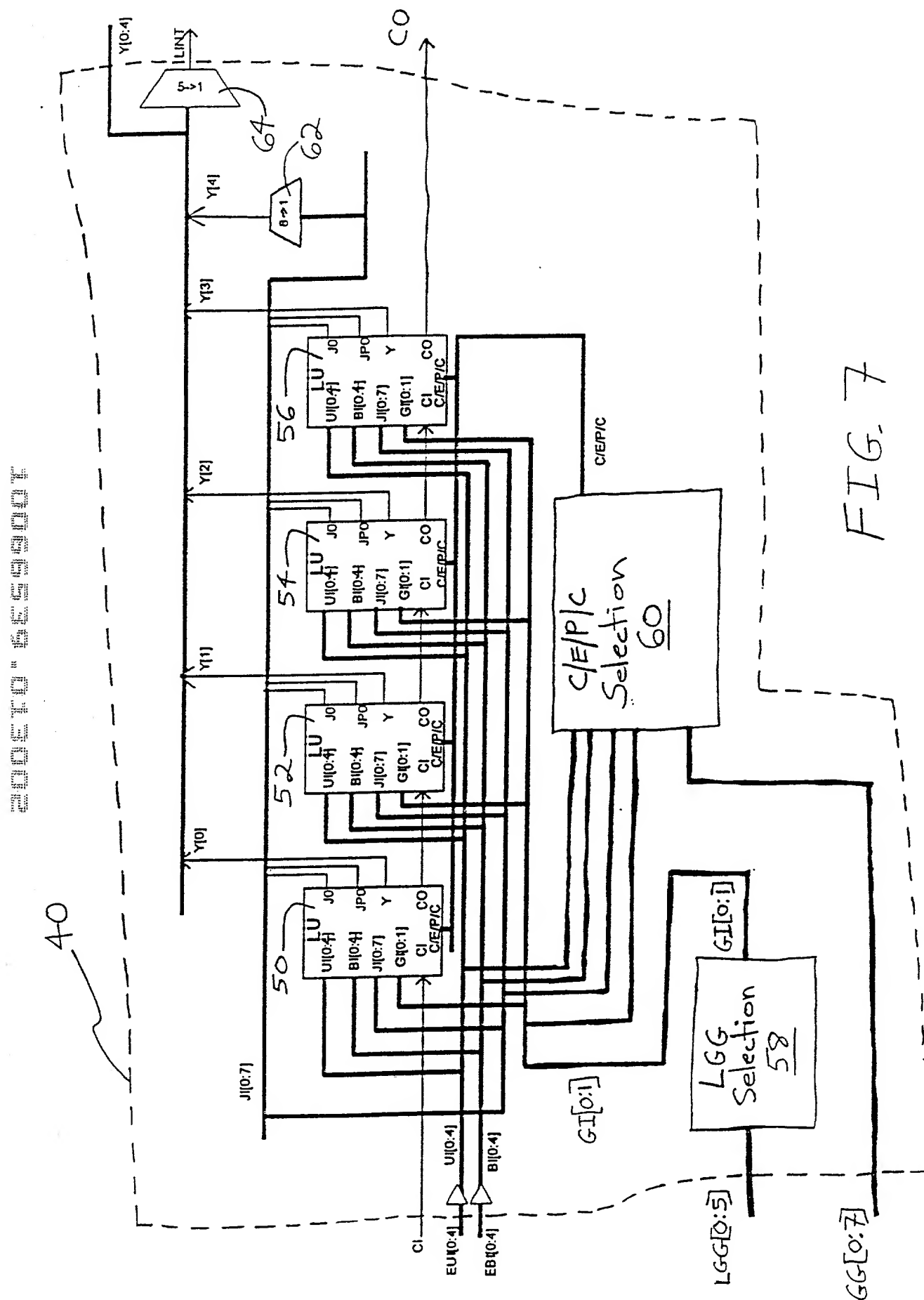


FIG. 6

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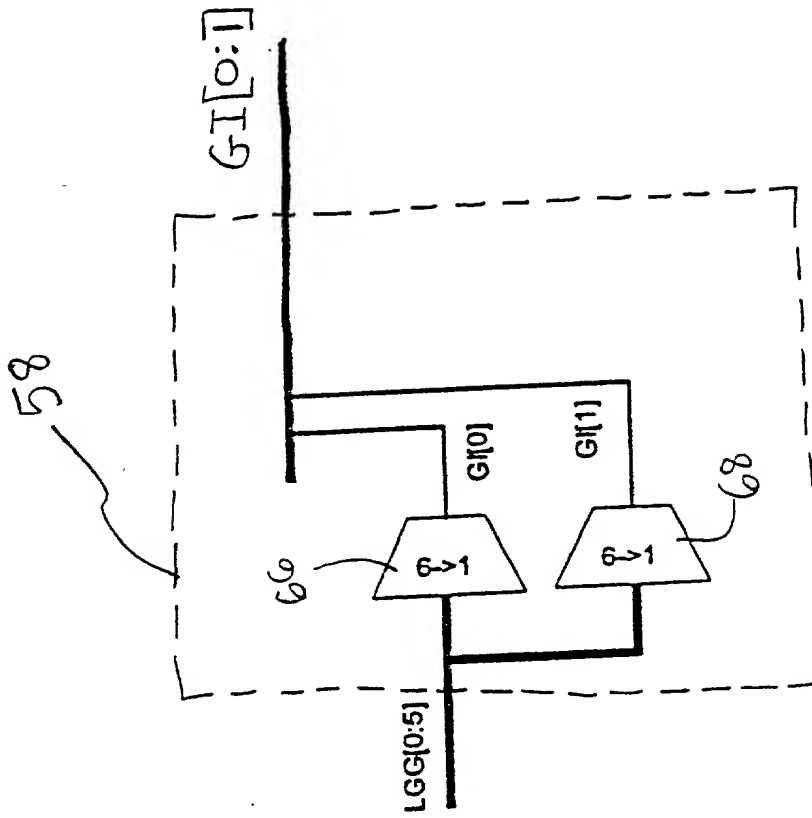


FIG. 8

FIG. 9

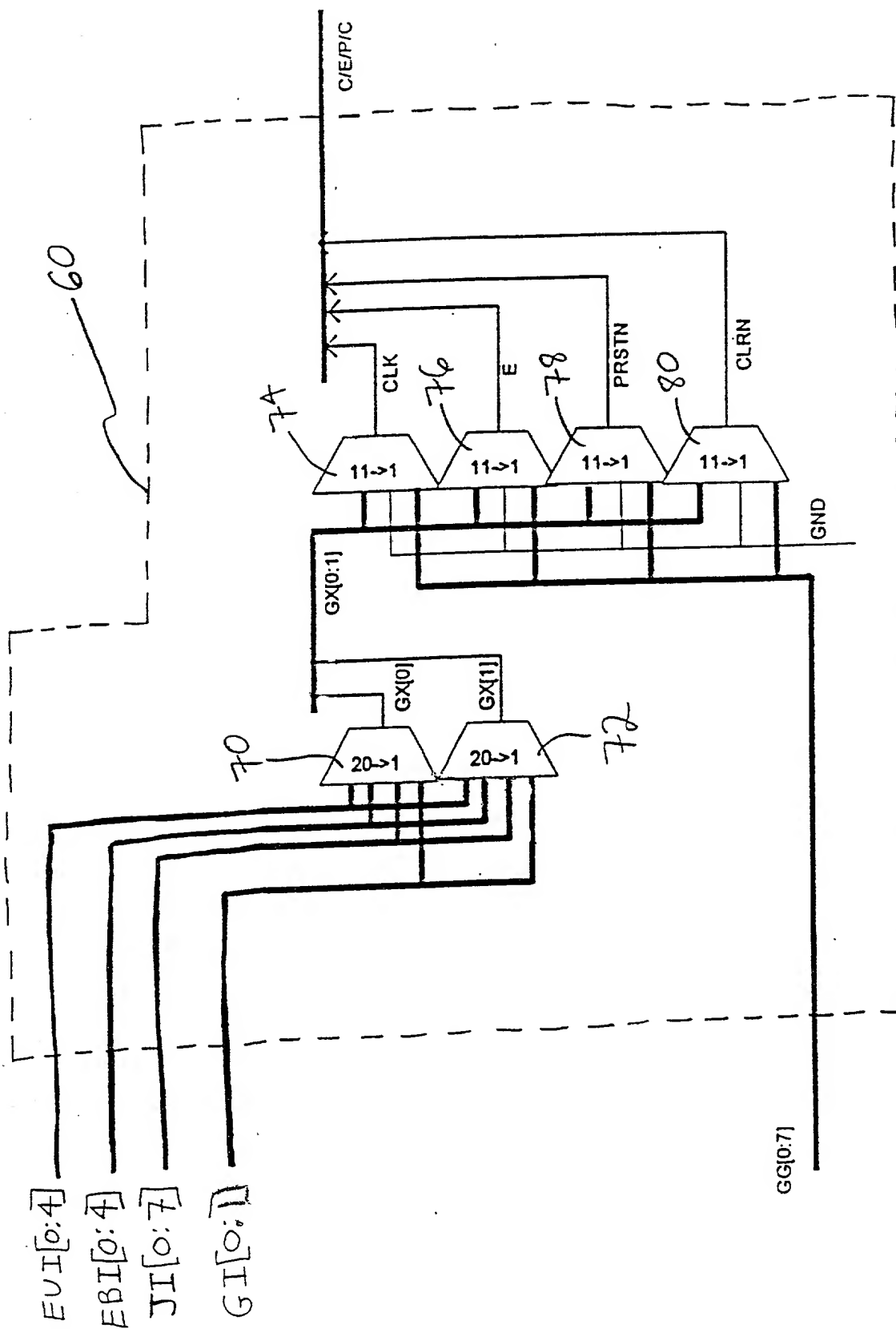


FIG. 9

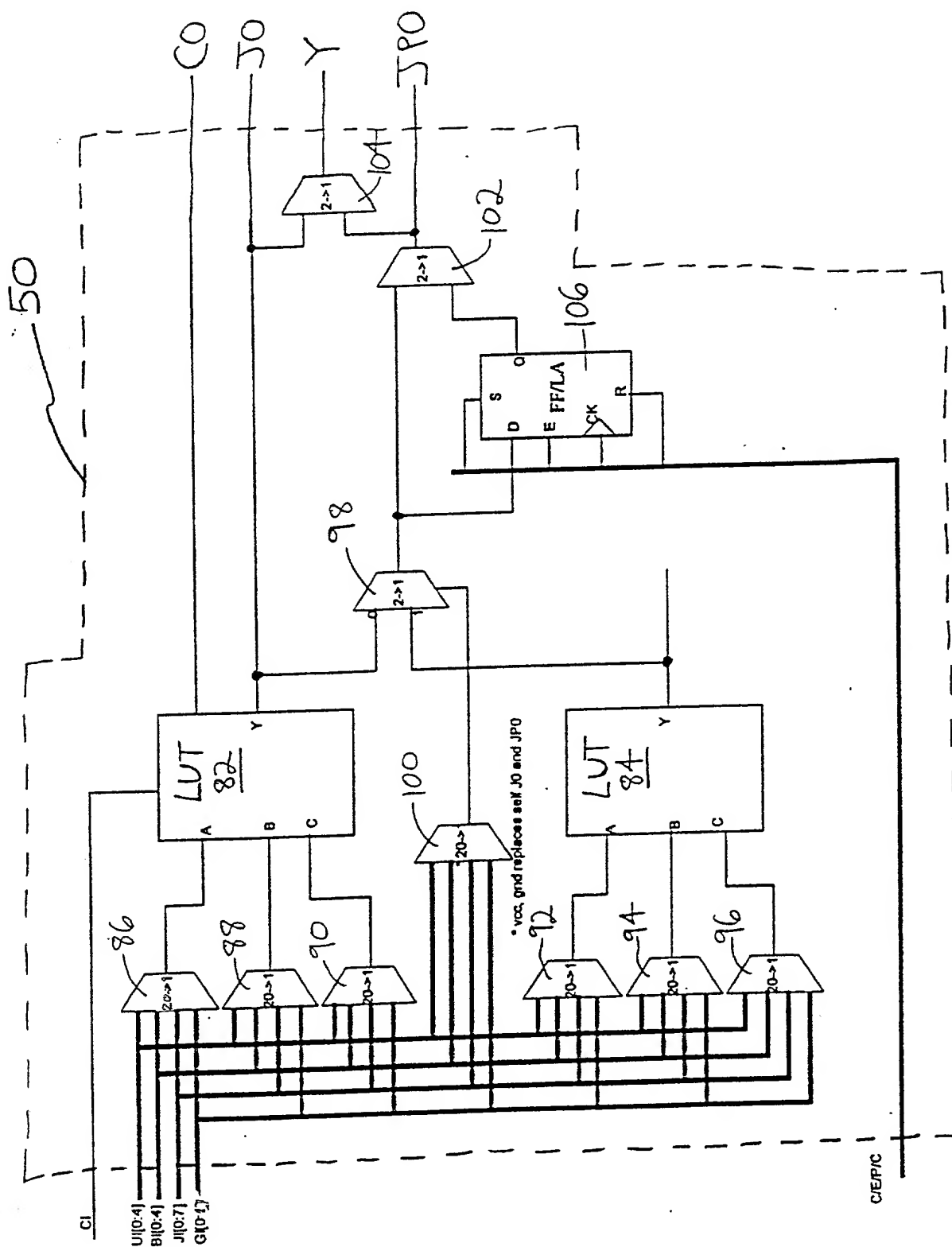


FIG. 10

2006-06-29

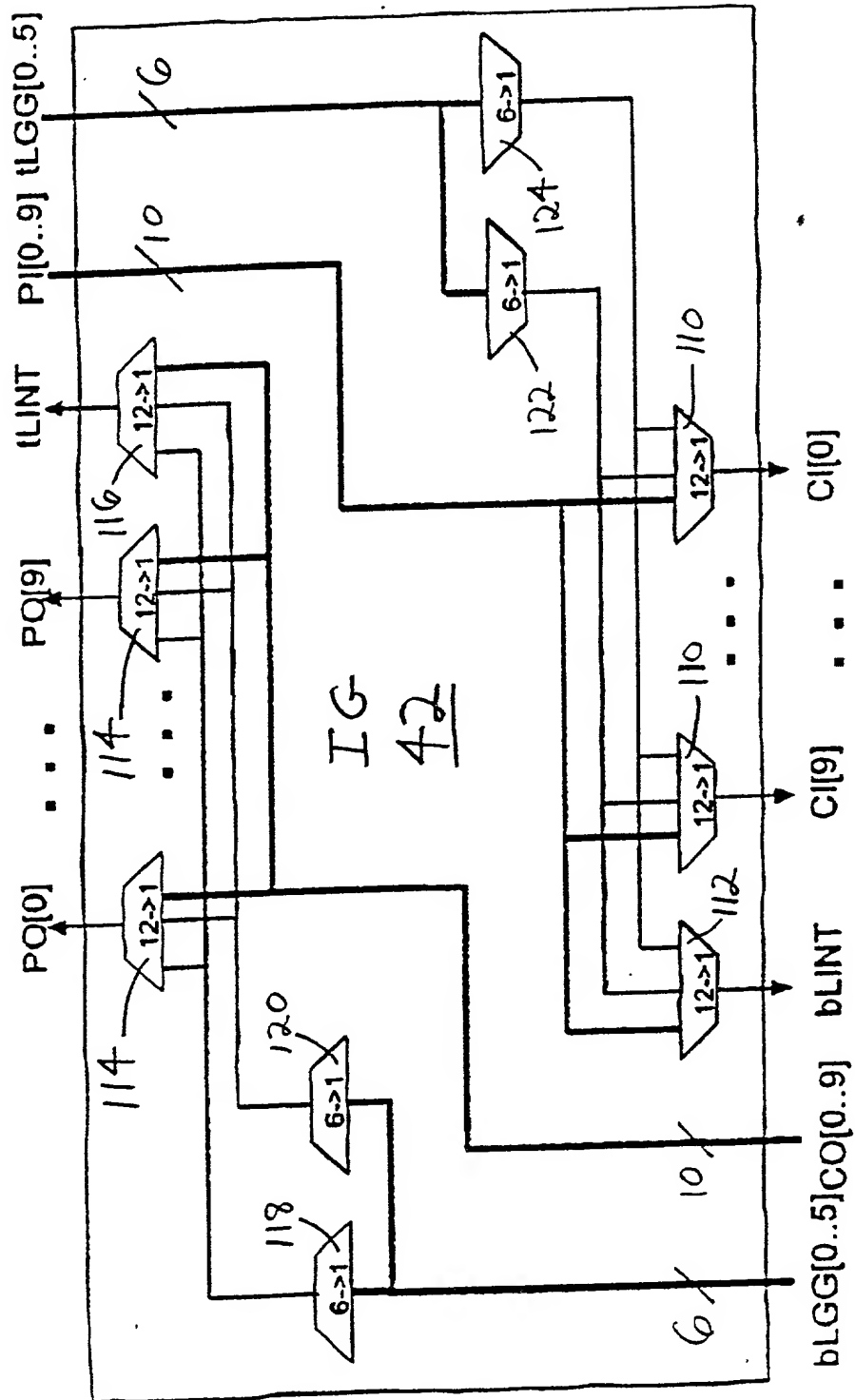


FIG. 11

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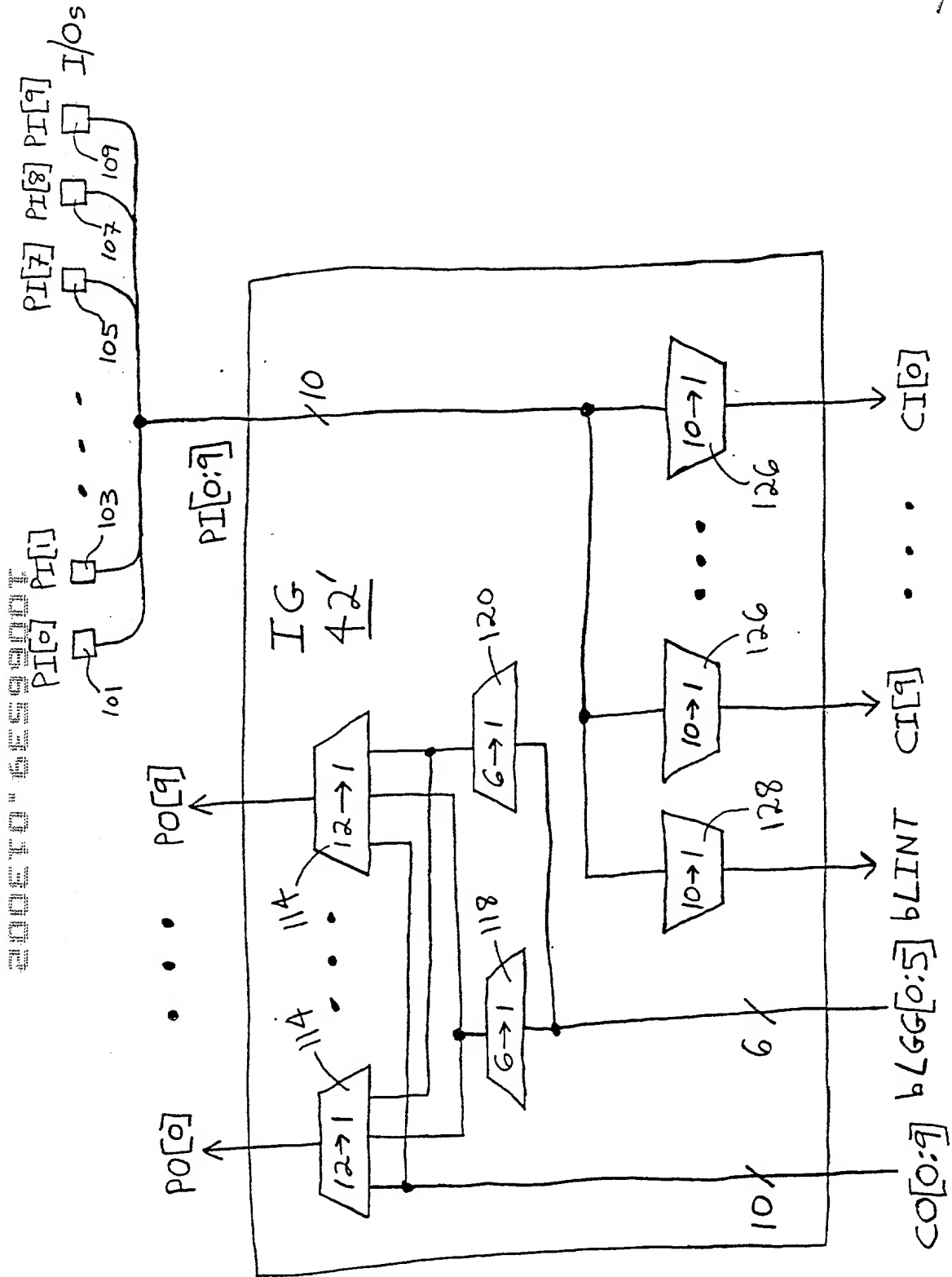
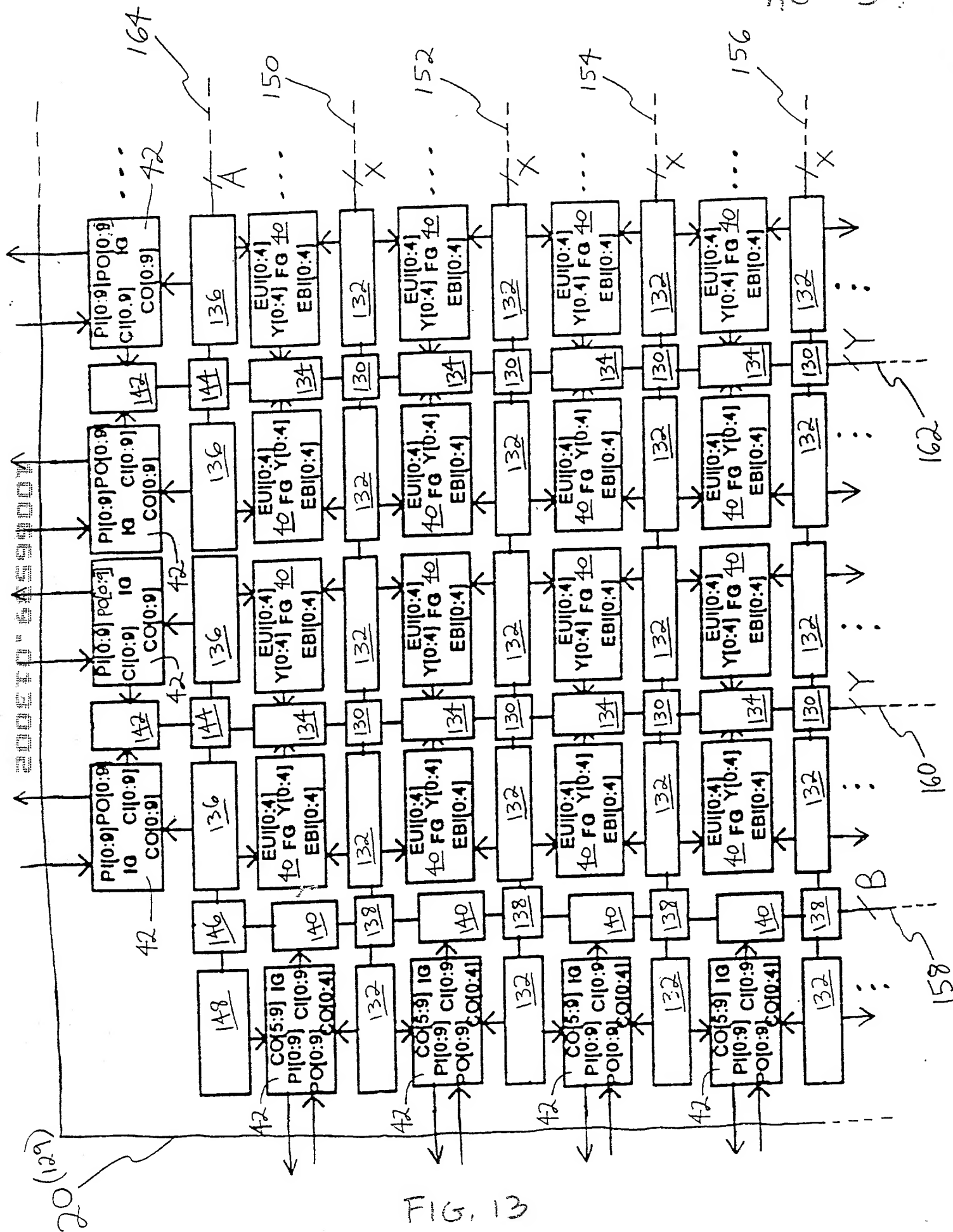


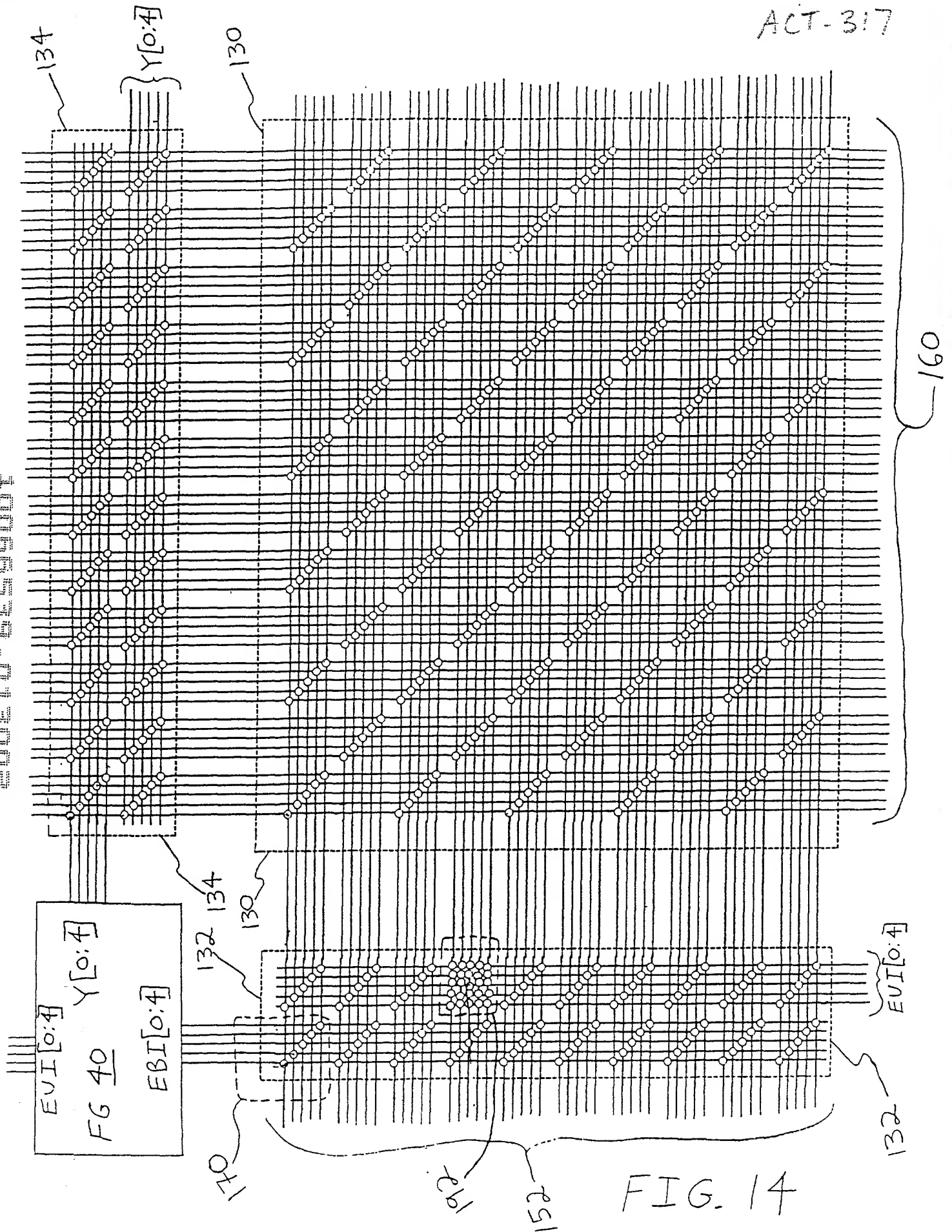
FIG. 12

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ACT-317

200210" 62599991



ACT-317

FIG. 15

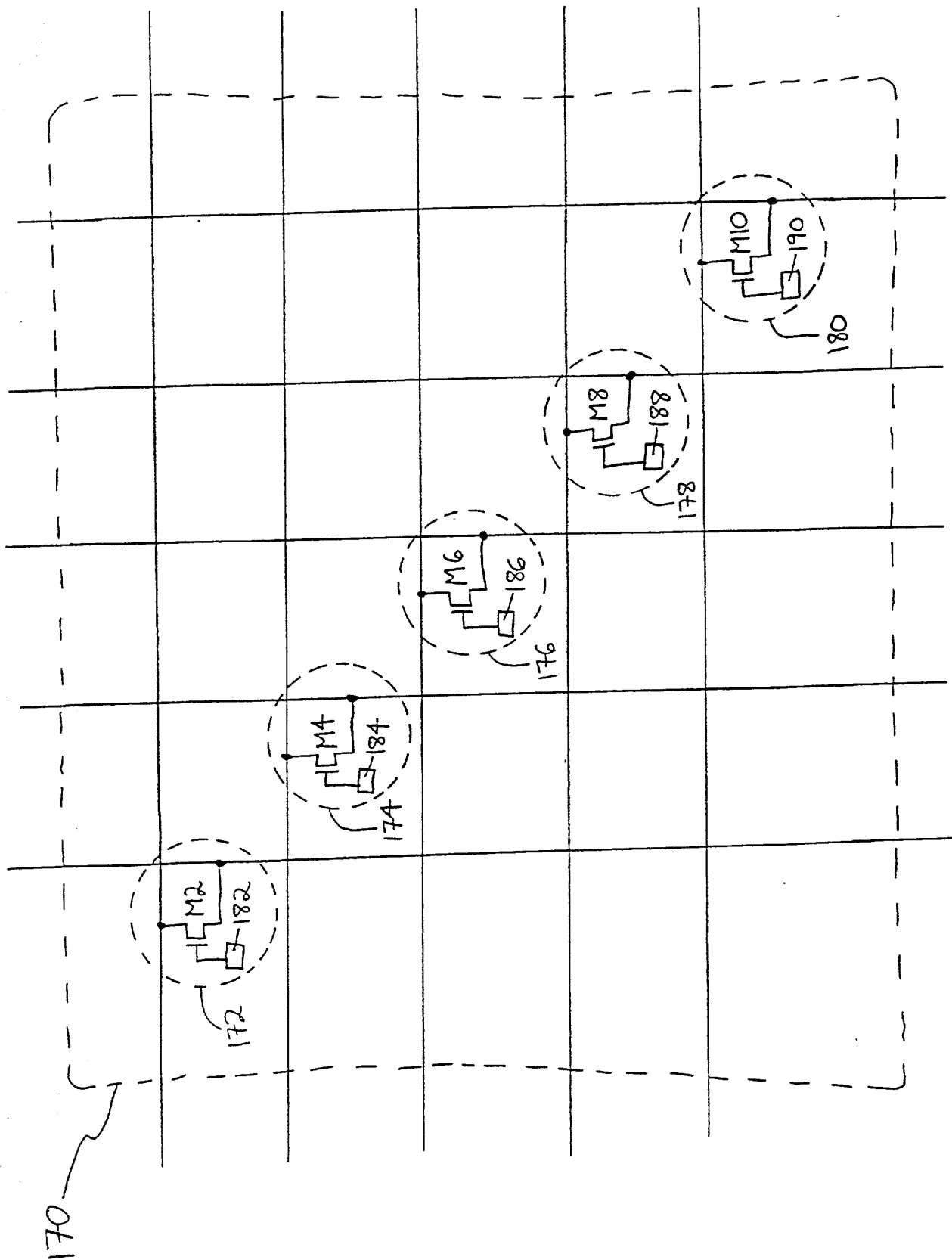


FIG. 15

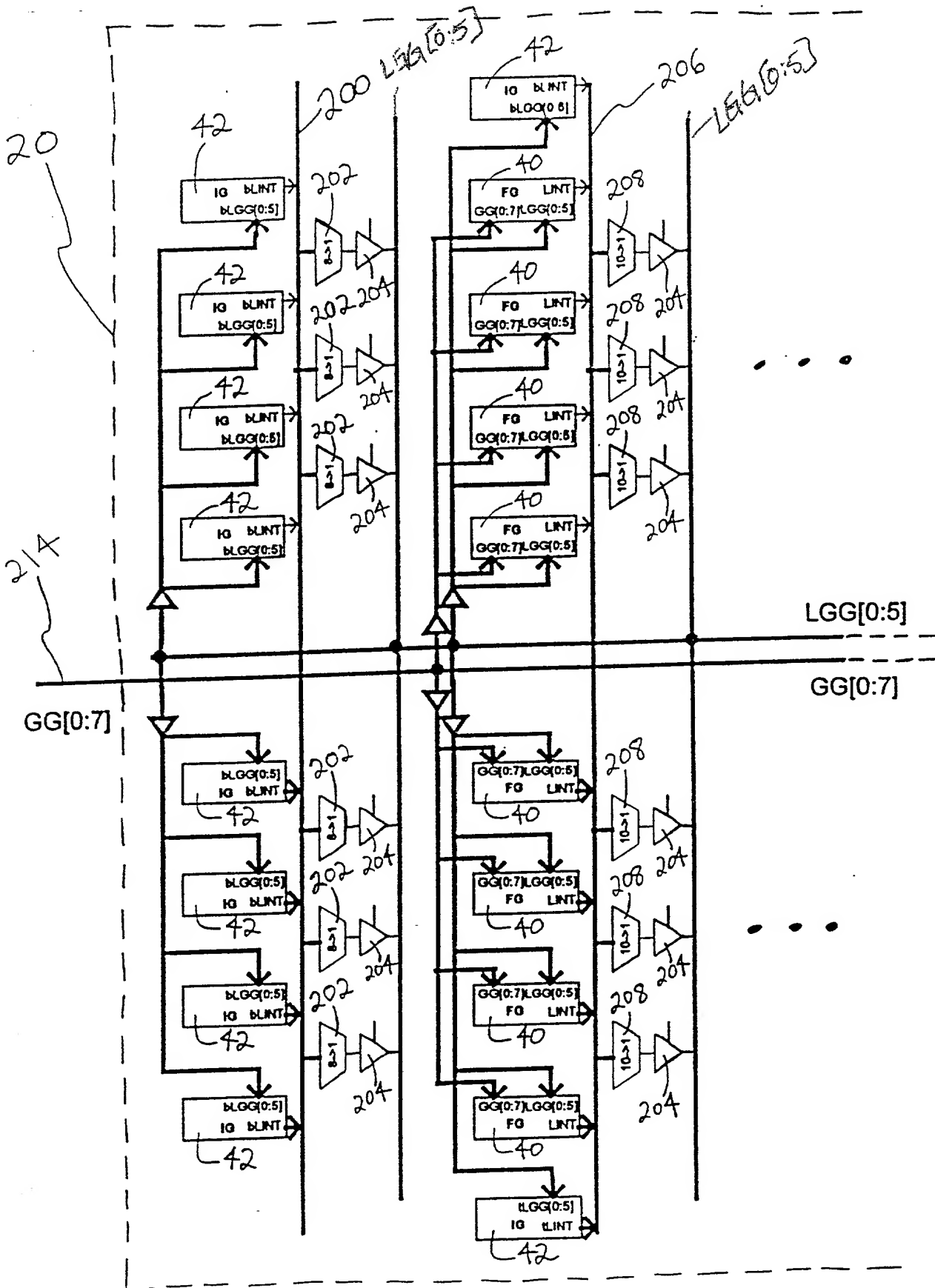


FIG. 16A

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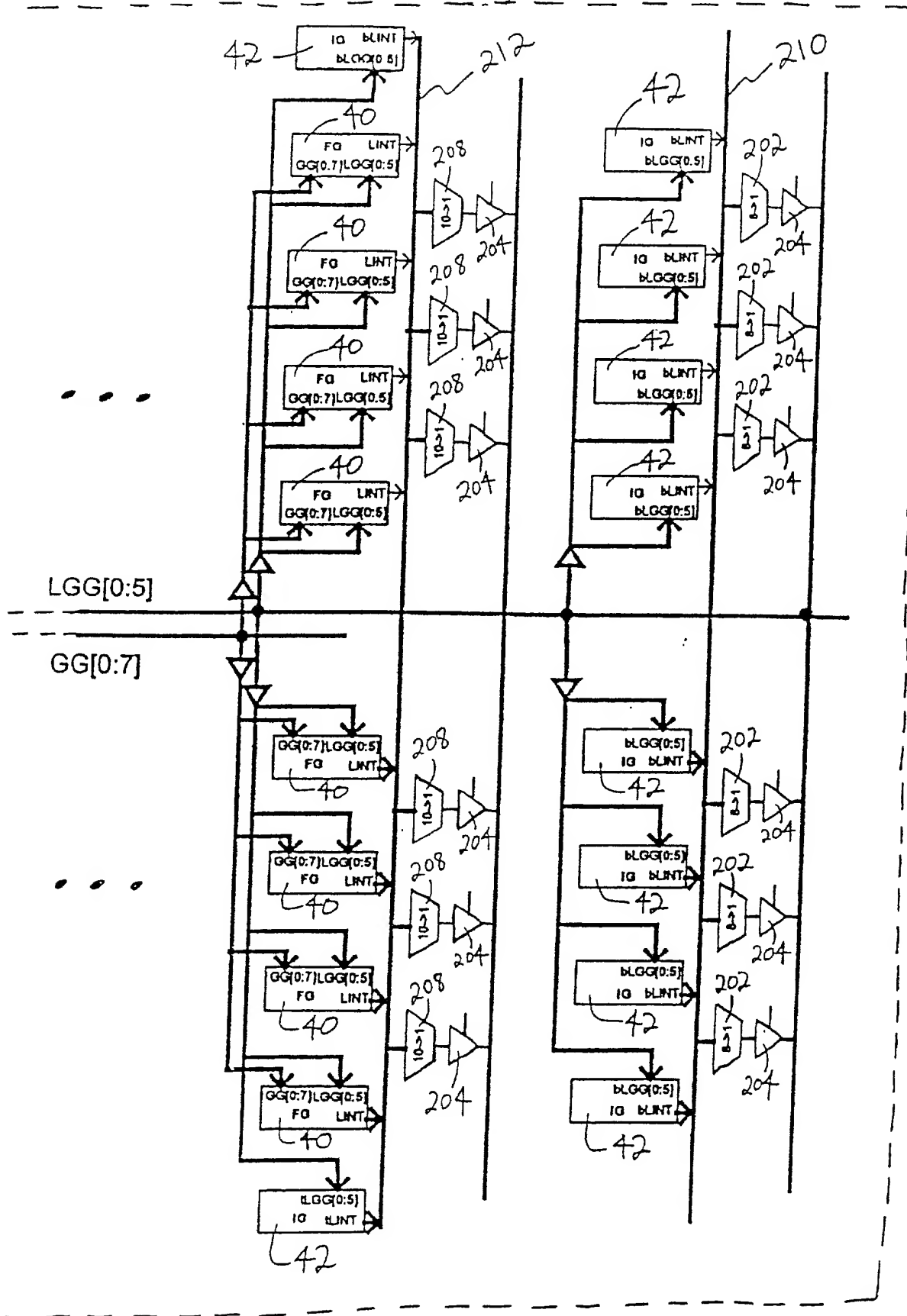


FIG. 16B

200606990001

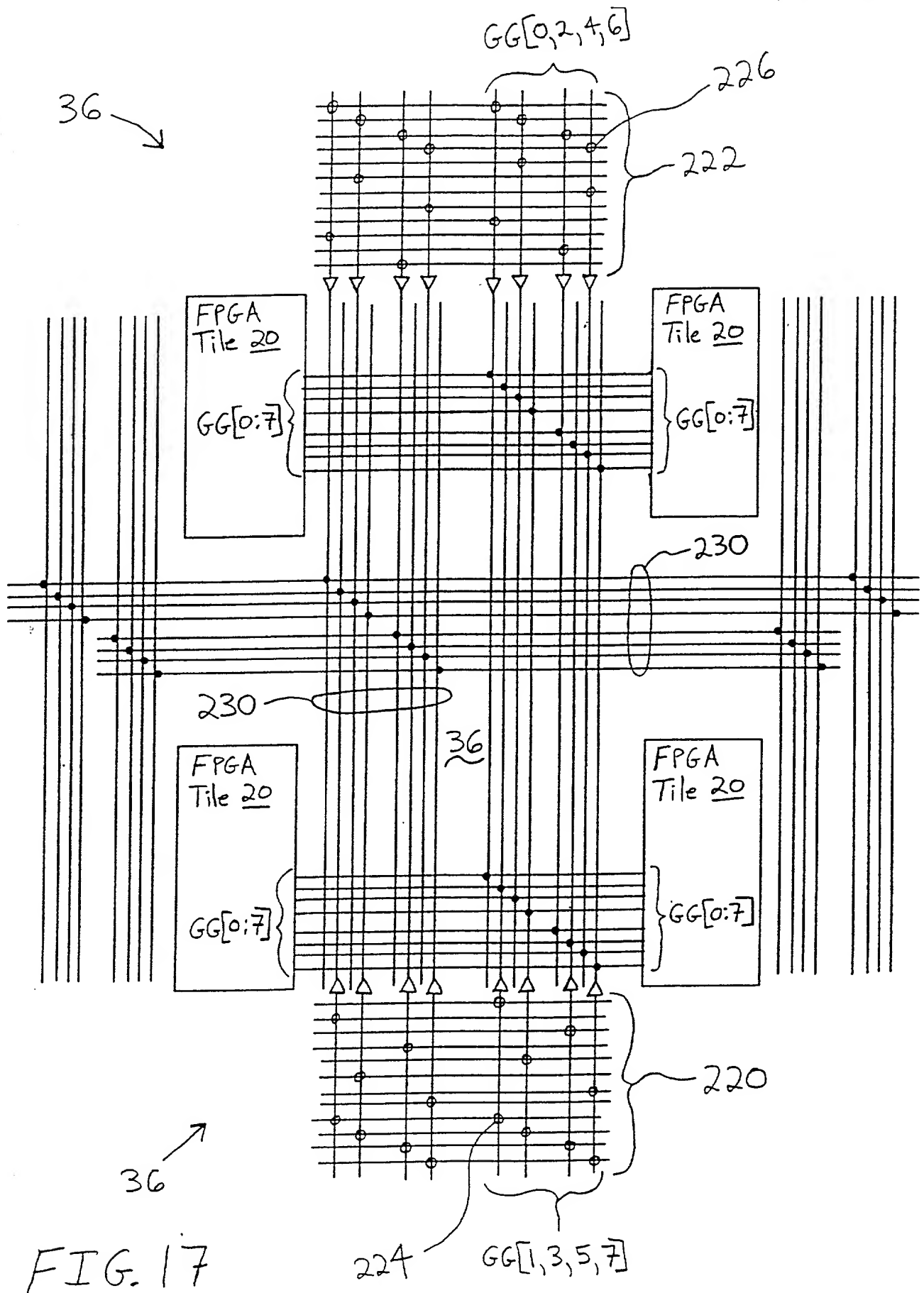


FIG. 17

1006696.01000

2025-06-09 09:00

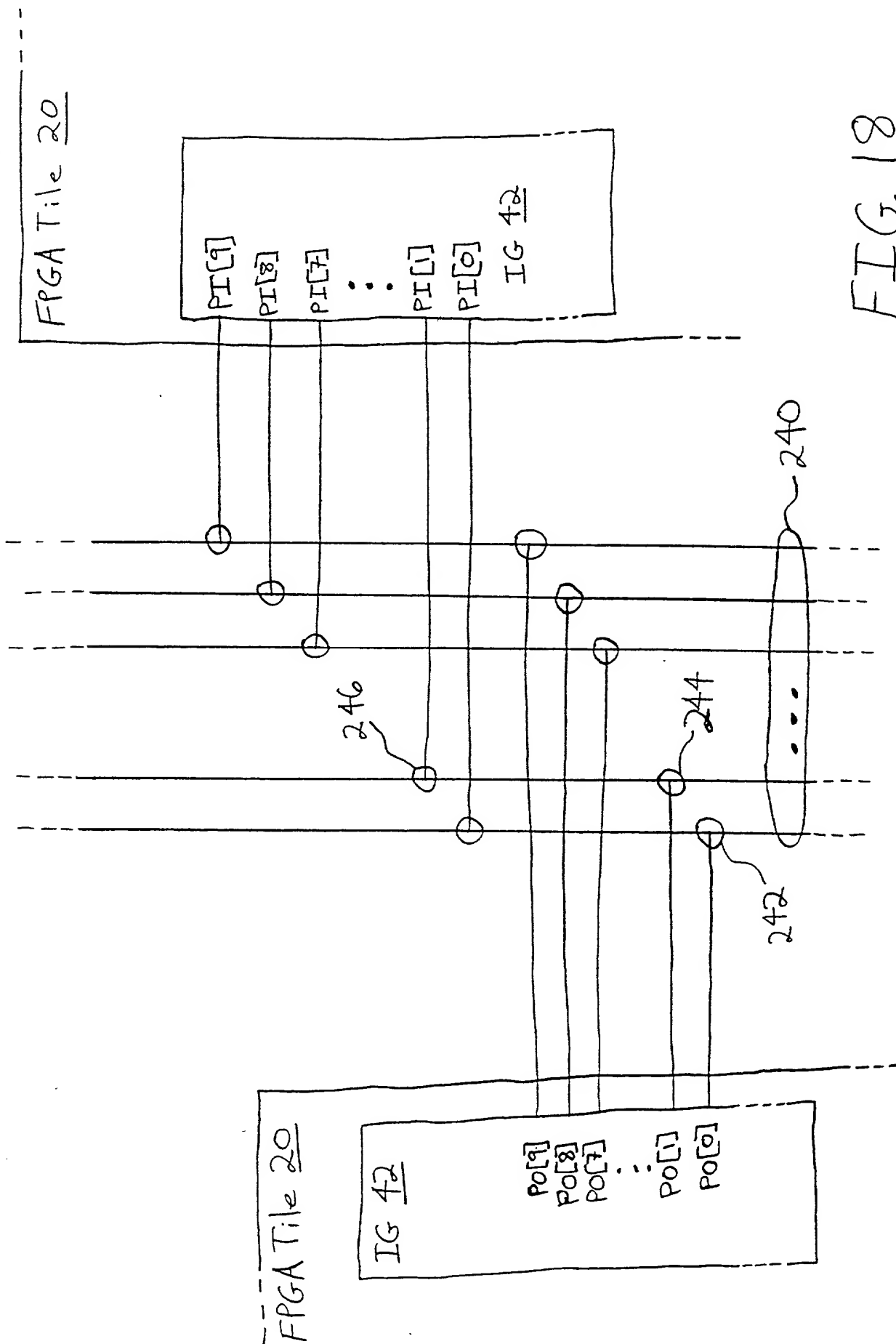


FIG. 18

2006-05-20

Vertical Track Testing Circuit

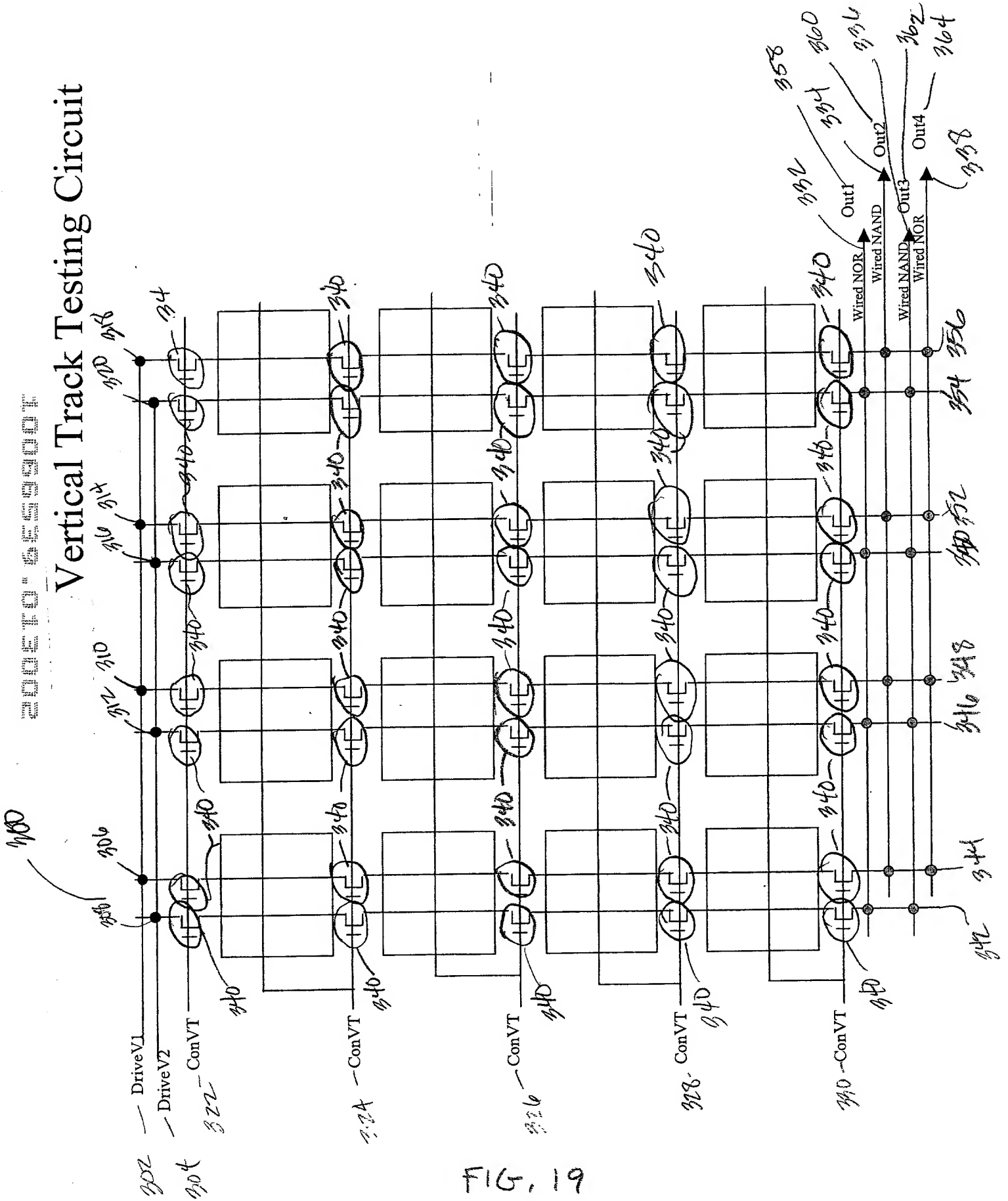


FIG. 19

Truth Table

DriveV1	DriveV2	Out1	Out2	Out3	Out4
0	0	1	1	1	1
0	1	1	0	1	0
1	0	0	1	0	1
1	1	0	0	0	0

FIG. 20

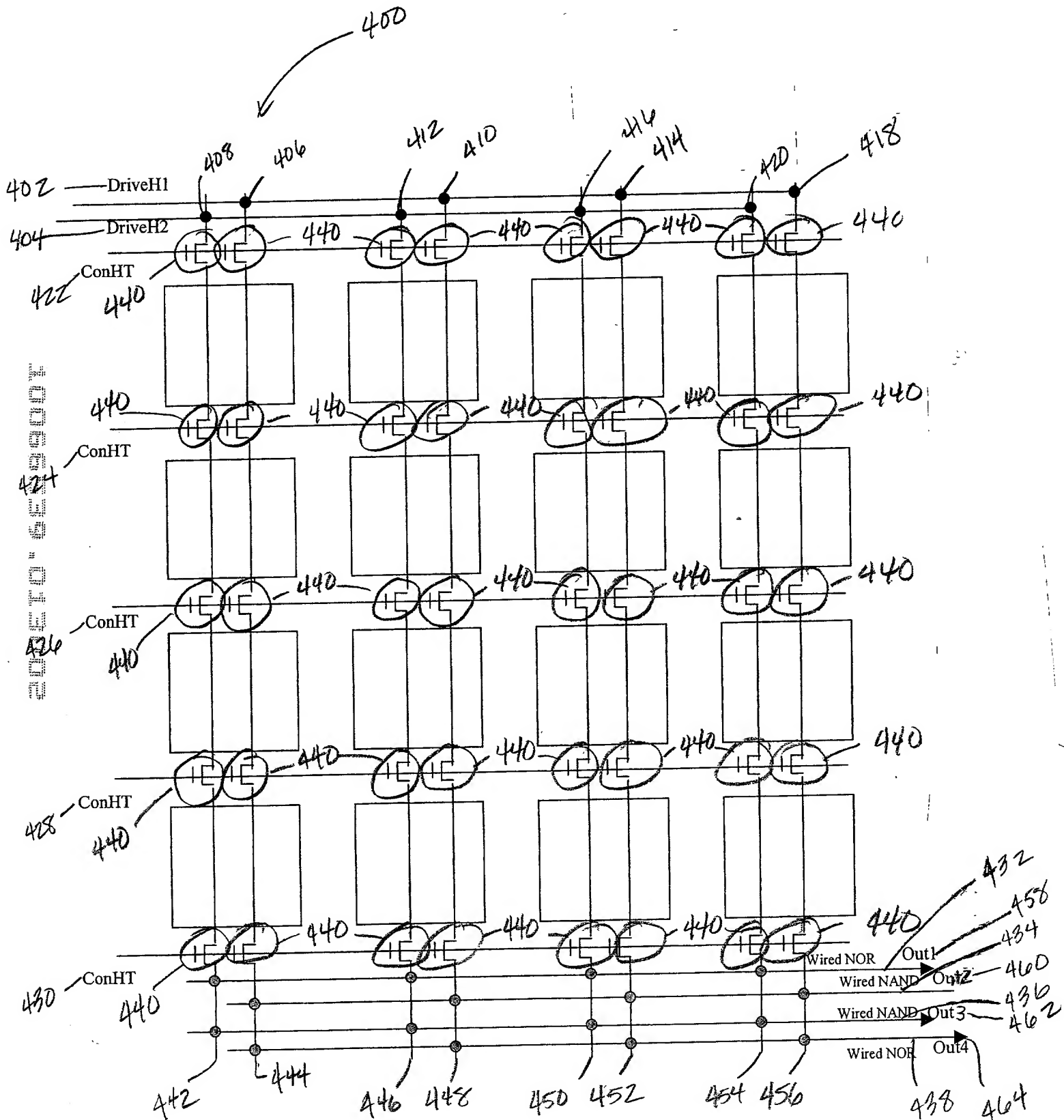


FIG. 21

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Truth Table

DriveH1	DriveH2	Out1	Out2	Out3	Out4
0	0	1	1	1	1
0	1	1	0	1	0
1	0	0	1	0	1
1	1	0	0	0	0

FIG 22